

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions and listings of claims in the application:

1. (Previously Presented) A power supply circuit, which has a scan driver power circuit for supplying a scan driver voltage to a scan driver for scanning a liquid crystal display device, and which has a data driver power circuit for supplying a data driver voltage to a data driver for sending display data to said liquid crystal display device, comprising:

*Don't*  
a brightness control circuit, provided in said scan driver power circuit, for controlling brightness of said liquid crystal display device by changing the voltage level of said scan driver voltage;

a voltage regulation circuit, provided in said data driver power circuit, for regulating the voltage level of said data driver voltage supplied to said liquid crystal display device to a predetermined value; and

a temperature compensation circuit, provided in said data driver power circuit, for compensating a temperature characteristic of said liquid crystal display device by changing the voltage level of said data driver voltage.

2. (Previously Presented) The power supply circuit according to claim 1, wherein said data driver power circuit further comprising:

an input power supply serving as a universal power supply therefore;

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an amplifying element having an input terminal connected to said input power supply, and having a control terminal and an output terminal from which the data driver power voltage is outputted; and

an impedance element connected between said input power circuit and said control terminal of said amplifying element, said voltage regulation circuit and said temperature compensation circuit being connected to said control terminal of said amplifying element.

3. (Previously Presented) The power supply circuit according to claim 2, wherein said voltage regulation circuit and said temperature compensation circuit comprise a diode group including a plurality of series-connected diodes connected between said control terminal of said amplifying element and ground.

4. (Previously Presented) The power supply circuit according to claim 3, wherein said series-connected diodes comprises a cathode terminal connected to said control terminal of said amplifying element and an anode terminal connected to the ground.

5. (Previously Presented) The power supply circuit according to claim 3, wherein the number of diodes of said diode group is determined from the sum of the voltage drop of each diode being approximately equal to said data driver voltage.

6. (Previously Presented) The power supply circuit according to claim 5, wherein the number of diodes of said diode group is seven.

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7. (Previously Presented) The power supply circuit according to claim 1, wherein said scan driver power circuit further comprising:

an input power supply serving as a universal power supply therefor;

an amplifying element having an input terminal connected to said input power supply, and having a control terminal and an output terminal from which the data driver power voltage is outputted;

a divider circuit, provided between said input power supply and the ground, for setting a voltage applied to said control terminal of said amplifying element; and

a variable resistor element, provided between the dividing point of said divider circuit and the control terminal of said amplifying element, which comprise said brightness control circuit.

8. (Previously Presented) The power supply circuit according to claim 7, wherein said divider circuit comprises:

a resistor having a terminal connected to said input power supply; and

a Zener diode having a cathode connected to said resistor and having an anode connected to ground.

9. (Previously Presented) The power supply circuit according to claim 2, wherein said amplifying elements are bipolar transistors.

10. (Previously Presented) The power supply circuit according to claim 2, wherein the impedance of said impedance element is within a range of 40 K $\Omega$  to 50 k $\Omega$ .

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11. (Previously Presented) The power supply circuit according to claim 3, wherein said diodes of said diode group are silicon diodes.

12. (Previously Presented) The power supply circuit according to claim 2, wherein said amplifying elements are MOS transistors.

13. (Previously Presented) The power supply circuit according to claim 2, wherein said amplifying elements are operational amplifiers.

14. (Original) The power supply circuit according to claim 2, wherein said amplifying elements are bipolar transistors.

15. (Original) The power supply circuit according to claim 2, wherein said amplifying elements are field effect transistors.

16. (Canceled).

17. (Canceled).

18. (Previously Presented) The power supply circuit according to claim 1, wherein the date driver power circuit performs, at the same time, a voltage regulation function, a temperature compensation function, and a power supply function for the liquid crystal display.

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